



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/692,347	10/23/2003	Kyoung-Youm Kim	5000-1-468	8526
33942	7590	03/10/2006	EXAMINER	
CHA & REITER, LLC 210 ROUTE 4 EAST STE 103 PARAMUS, NJ 07652			DUPUIS, DEREK L	
			ART UNIT	PAPER NUMBER
			2883	

DATE MAILED: 03/10/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

82

Office Action Summary	Application No. 10/692,347	Applicant(s) KIM ET AL.	
	Examiner Derek L. Dupuis	Art Unit 2883	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 13 February 2006.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-19 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-19 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 09 January 2006 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 1/9/2006 has been entered.

Response to Arguments

2. Applicant's arguments filed 1/9/2006 have been fully considered but they are not persuasive. Applicant argues on page 12 of the remarks that the groove in Seki et al is formed "to extend along the entire width of the substrate". Applicant also argues that Seki does not anticipate the invention because "the reflective grooves are structurally different than the grooves of the present invention." The examiner disagrees with this assertion. The grooves taught by Seki et al anticipate the claimed invention. The applicant claims that the groove is "disposed at one end surface of the bidirectional optical communication module". Clearly, the groove of Seki et al is disposed at one end (11). It is also disposed at another end (12). The language of the applicant's claim is such that it does not limit the location of the groove to *exclusively* one single end surface. Furthermore, the groove (30B) of Seki et al is clearly coupled to the connection waveguide (23B).

3. Applicant also argues that "a reflective layer is not disclosed in Seki as formed on a base surface of the reflective groove." The examiner disagrees with this argument. The reflective layer (40B) is clearly shown as being inside the base surface of the groove (30B).

Art Unit: 2883

4. In page 13 of the remarks applicant argues that claims 1-19 are product by process claims. The examiner agrees with this assertion. However, the applicant argues that the process of photolithography imparts structural limitations to the claim because the office action "has not met its initial burden of presenting a prima facie case of unpatentability". The examiner disagrees. As explained above, Seki et al anticipates at least claims 1 and 11 of the present invention. Therefore, the end product is not novel and non-obvious.

Claim Rejections - 35 USC § 102

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

6. Claims 1, 9, 11, and 14 are rejected under 35 U.S.C. 102(b) as being anticipated by *Seki et al (US 4,790,615)*.

7. Regarding claim 1, Seki et al teach a bidirectional optical communication module as shown in figure 2. The module includes an input waveguide (25A) for inputting an optical signal and a reflector (40B) having a reflective groove (30B) that is disposed at one end surface (11 or 12) of the module. A reflective layer (40B) is formed on a base surface of the reflective groove (30B) to reflect the optical signal inputted from the input waveguide (25A) to an output waveguide (25B) (see column 3, lines 13-63). At the connection waveguide (23B), light is sent from the input waveguide (25A), to the reflector (40B), and is then outputted to the output waveguide (25B).

Art Unit: 2883

8. Regarding claim 9, Seki et al teach a module as discussed above in reference to claim 1.

Seki et al teach that the module further comprises a multiplexer (40A), a silicon substrate (10), and a cladding layer layer stacked on the substrate (20). The waveguides, the multiplexer, and the groove are all formed on the cladding layer (see column 3, lines 13-26).

9. Regarding claim 11, Seki et al teach a bidirectional optical communication module as shown in figure 2. The module includes a multiplexer (23A) connected to a first waveguide (22) for outputting or inputting a multiplexed signal and two second waveguides (24A and 25A) for inputting or outputting a demultiplexed optical signal. The module includes a reflective layer (40B) connected to a terminal of one of the second waveguides (25A) for reflecting an optical signal. A third waveguide (25B) is connected to the reflective layer for inputting or outputting an optical signal reflected by the reflective layer. The reflective layer is formed on a base surface of a reflective groove (30B) that is disposed at one end surface (11 or 12) of the module.

10. Regarding claim 14, Seki et al teach a module as discussed above in reference to claim

12. Seki et al teach that a connection waveguide (23B) is used to input the optical signal to the reflective layer and to output the optical signal from the reflective layer. The two waveguides connected to the connection waveguides are overlapped at a predetermined angle 2θ as shown in figure 2.

11. Claims 1-19 are **product-by-process claims**:

Note that a "product by process" claim is directed to the product per se, no matter how actually made, *In re Hirao*, 190 USPQ 15 at 17 (footnote 3). See also *In re Thorpe*, 227 USPQ 964, 966; *In re Luck*, 177 USPQ 523; *In re Fessmann*, 180 USPQ 324; *In re Avery*, 186 USPQ 161; *In re Wertheim*, 191 USPQ 90 (209 USPQ 554 does not deal with this issue); and *In re Marosi et al.*, 218 USPQ 289, all of which make it clear that it is the patentability of the final product per se which must be determined in a "product by process" claim, and not the patentability of the process, and that an old or obvious product produced by a new method is not patentable as a product, whether claimed in "product by process" claims or not. Note that applicant has the burden of proof in such cases, as the above case law makes clear. See also MPEP 2113.

12. Claims 1 and 11 do not distinguish over the Seki et al reference regardless of the process used to form the reflective groove, because only the final product is relevant, and not the process of making such as photolithography.

Claim Rejections - 35 USC § 103

13. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

14. Claims 2-5, 7, 8, 12, 13, and 15-18 are rejected under 35 U.S.C. 103(a) as being unpatentable over *Seki et al (US 4,790,615)* as applied to claims 1, 9, 11, and 14 above.

15. Regarding claims 2, 4, 15, and 17, Seki et al teach a module as discussed above in reference to claims 1 and 11. Seki et al define a "filter angle" θ as the angle between the optical paths and the plane that is normal to the filter as is shown in figure 2. The angle between the two arms would be equal to 2θ . Seki et al teach in table 3 and in figure 9A and 9B that the angle can be between 0 degrees and 20 degrees. This means that the angle of the arms can be between 0 degrees and 40 degrees.

In the case where the claimed ranges "overlap or lie inside ranges disclosed by the prior art" a prima facie case of obviousness exists. In re Wertheim, 541 F.2d 257, 191 USPQ 90 (CCPA 1976); In re Woodruff, 919 F.2d 1575, 16 USPQ2d 1934 (Fed. Cir. 1990). See MPEP 2144.05.

16. Regarding claims 3, 5, 16, and 18, Seki et al teach a module as discussed above in reference to claims 2, 4, 15, and 17. Seki et al do not teach that the location of the base surface is

Art Unit: 2883

limited by the range defined by the claimed equations. However, it would have been obvious to one of ordinary skill in the art at the time of invention to limit the location of the base surface by the range defined by the claimed equations since it has been held that where the general conditions of a claim are disclosed in the prior art, discovering the optimum or workable ranges involves only routine skill in the art. *In re Boesch*, 617 F.2d 272, 205 USPQ 215 (CCPA 1980).

17. Regarding claims 7 and 8, Seki et al teach a module as discussed above in reference to claim 1. The input waveguide (25A) is attached to a multiplexer (23A) and the output waveguide (25B) can be connected to a light receiving element. Waveguide 25B can be connected to a light source, and waveguide 25B would then be referenced as an input waveguide and waveguide 25A would then be referenced as an output waveguide (which is connected to a multiplexer (23A)).

It would have been obvious to one of ordinary skill in the art at the time of invention to modify the module of Seki et al by coupling the waveguide 25B to a light source or alternatively, to a light detector. Motivation to do this is the suggestion by Seki et al that the waveguides 21, 24A, 24B, and 25B can each be coupled to a light emitting device or a light receiving device (see column 3, lines 47-55). Light sources and light detectors are commonly used examples of these devices that are well known in the art.

18. Regarding claims 12 and 13, Seki et al teach a module as discussed above in reference to claim 11. One of the second waveguides (24A) is attached to light emitting element and the third waveguide (25B) is connected to a light receiving element. Alternatively, waveguide 25B could be connected to a light emitting element, and waveguide 24A could be connected to a light receiving element.

It would have been obvious to one of ordinary skill in the art at the time of invention to modify the module of Seki et al by coupling the waveguide 25B to a light source or alternatively, to a light detector and by coupling the waveguide 24A to a light detector or alternatively, to a light source. Motivation to do this is the suggestion by Seki et al that the waveguides 21, 24A, 24B, and 25B can each be coupled to a light emitting device or a light receiving device (see column 3, lines 47-55). Light sources and light detectors are commonly used examples of these devices that are well known in the art.

19. Claim 6 is rejected under 35 U.S.C. 103(a) as being unpatentable over *Seki et al (US 4,790,615)* as applied to claim 1 above and in further view of *Kimura et al (US 2002/0048431 A1)*.

20. Regarding claim 6, Seki et al teach a module as discussed above in reference to claim 1. Seki et al teach that the reflective layer is attached on the base of the surface formed by the reflective groove (see column 4, lines 10-25). While Seki et al do not explicitly state that the reflective layer is a metal layer, Kimura et al teach that the reflective layer is a dielectric multilayer filter (see column 5, lines 8-35). It is well known in the art that many dielectric filters include silicon, which is classified as semi-metallic. Other well known filters have gold and silver metallic layers to reflect lights of a desired wavelength. Seki et al also suggests that the filter can be made of titanium (see column 7, line 63 to column 8, line 6).

It would have been obvious to one of ordinary skill in the art at the time of invention that the filter of Kimura et al could be a metal layer because filters comprising metal layers are well known in the art as has been demonstrated by Kimura et al.

Art Unit: 2883

21. Claims 10 and 19 are rejected under 35 U.S.C. 103(a) as being unpatentable over *Seki et al (US 4,790,615)* as applied to claims 1, 9, 11, and 14 above, and further in view of *Kitamura (US 6,480,647 B1)*.

22. Regarding claims 10 and 19, Seki et al teach a module as discussed above in reference to claims 1 and 11. Seki et al teach that the optical multiplexer device is a filter. Kitamura teaches several multiplexers used in bidirectional modules, including a directional coupler (figure 4), an MMI coupler (figure 5), and a filter (figure 6).

It would have been obvious to one of ordinary skill in the art at the time of invention to modify the module of Seki et al by using a directional coupler or an MMI multiplexer in place of the filter as taught by Kitamura since Kitamura teaches that a directional coupler or an MMI coupler is an art recognized equivalent of a filter for purposes of multiplexing a signal.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Derek L. Dupuis whose telephone number is (571) 272-3101. The examiner can normally be reached on Monday - Friday 8:30am-4:30pm.

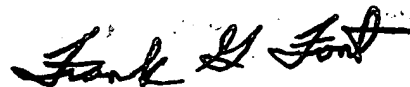
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Frank G. Font can be reached on (571) 272-2415. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2883

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Derek L. Dupuis
Group Art Unit 2883



Frank G. Font
Supervisory Patent Examiner
Technology Center 2800